

**INTEGRAL UNIVERSITY, LUCKNOW**  
**DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING**  
**Reassessment Schedule, Session 2019-2020 (ODD Semester)**

**1st Shift: (9:15 am to 10:45 am), 2nd Shift: (11:00 am to 12:30 pm),**  
**3rd Shift: (2:00pm to 3:30 pm)**

**B.Tech Second Semester (EC)**

<b>Date</b>	<b>Day</b>	<b>1<sup>st</sup> Shift 09:15 am to 10:45 am</b>	<b>2<sup>nd</sup> Shift 11:00 am to 12:30 pm</b>	<b>3<sup>rd</sup> Shift 2.00 pm to 3:30 pm</b>
25- Nov-19	Monday	Mathematics II (MT 112)	Environmental Studies (ES 101)	Basic Mechanical Engg. (ME 101)
26- Nov-19	Tuesday	Computer Programming (CS 101)	Chemistry (CH 101)	-

**B.Tech Fourth Semester (EC)**

<b>Date</b>	<b>Day</b>	<b>1<sup>st</sup> Shift 09:15 am to 10:45 am</b>	<b>2<sup>nd</sup> Shift 11:00 am to 12:30 pm</b>	<b>3<sup>rd</sup> Shift 2.00 pm to 3:30 pm</b>
25- Nov-19	Monday	Programming Concepts (EC 208)	Digital Electronics (EC 209)	Signals and Systems (EC 210)
26- Nov-19	Tuesday	Measurement and Instrumentation (EC 211)	Semiconductor Material and Power Devices (EC 212)	Management Concepts in Engineering (BM 227)

**B.Tech Sixth Semester (EC)**

<b>Date</b>	<b>Day</b>	<b>1<sup>st</sup> Shift 09:15 am to 10:45 am</b>	<b>2<sup>nd</sup> Shift 11:00 am to 12:30 pm</b>	<b>3<sup>rd</sup> Shift 2.00 pm to 3:30 pm</b>
25- Nov-19	Monday	Digital Communication & Networks (EC-312)	Digital Signal Processing (EC-313)	"Microprocessor and Applications (EC-314)"
26- Nov-19	Tuesday	<b>Elective-2</b> Nano Electronics (EC-315)/ Embedded System Design (EC-316)/ Analog Signal Processing (EC-317)/ Filter Design (EC-318)	Open Elective	<b>Elective-3</b> RF and Microwave Engineering (EC-319)/ Microwave Devices and Communication (EC-320)/ Solar Engineering (EC-339)/ Wind Engineering (EC-340)

### B.Tech Eighth Semester (EC)

<b>Date</b>	<b>Day</b>	<b>1<sup>st</sup> Shift 09:15 am to 10:45 am</b>	<b>2<sup>nd</sup> Shift 11:00 am to 12:30 pm</b>	<b>3<sup>rd</sup> Shift 2.00 pm to 3:30 pm</b>
25- Nov-19	Monday		Open elective	
26- Nov-19	Tuesday			

### M.Tech Second Semester (ECS)

<b>Date</b>	<b>Day</b>	<b>1<sup>st</sup> Shift 09:15 am to 10:45 am</b>	<b>2<sup>nd</sup> Shift 11:00 am to 12:30 pm</b>	<b>3<sup>rd</sup> Shift 2.00 pm to 3:30 pm</b>
25- Nov-19	Monday	VLSI Design (DC) (EC 506)	Analog MOS Circuits (EC 507)	
26- Nov-19	Tuesday	ASIC Design & FPGA (DC) (EC 508)	Fault Modeling & Testing of Electronics Circuits (DC) (EC 509)	